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| TRANSMITTAL FORM <i>(to be used for all correspondence after initial filing)</i> | Application / Conf. No. | 10/783,216 / |
| | Filing Date | February 20, 2004 |
| | First Named Inventor | Tim Tuan |
| | Examiner Name | Unknown |
| | Art Unit | Unknown |
| | Patent No. | |
| Mail Stop: | | |
| Express Mail Receipt No. | | |
| Total Number of Pages in This Submission | | Attorney Docket Number X-1462-1P US |

| ENCLOSURES (check all that apply) | | |
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| <input type="checkbox"/> Preliminary Amendment | <input type="checkbox"/> Drawing(s) | <input type="checkbox"/> Appeal Communication to Group (Appeal Notice, Brief, Reply Brief) |
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| <input checked="" type="checkbox"/> Substitute PTO-1449(s) IDS by Applicant (PTO/SB/08A) | Remarks | |
| <input type="checkbox"/> Certified Copy of Priority Document(s) | | |
| <input type="checkbox"/> Response to Missing Parts/ Incomplete Application | | |
| <input type="checkbox"/> Response to Missing Parts under 37 CFR 1.52 or 1.53 | | |

SIGNATURE OF APPLICANT, ATTORNEY, OR AGENT

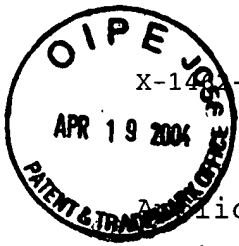
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| Firm or Customer Number | 24309 (Customer Number) | Reg. Number 51,959 |
| Attn: Justin Lin | | |
| Signature | | |
| Date | April 16, 2004 | Charge any additional fees required/credit any overpayment to our Deposit Account Number: 24-0040 |

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| Typed or Printed Name | Julie Matthews | | |
| Signature | | Date | April 16, 2004 |

This collection of information is required by 37 CFR 1.17 and 1.27. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 12 minutes to complete. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, Virginia 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, Virginia 22313-1450.



X-1462-1P US

PATENT

IN THE UNITED STATES PATENT OFFICE

Applicants: Tim Tuan et al.
Assignee: Xilinx, Inc.
Title: "Tuning Programmable Logic Devices for Low-Power Design Implementation"

Serial No.: 10/783,216 File Date: February 20, 2004
Examiner: Unknown Art Unit: Unknown
Docket No.: X-1462-1P US

COMMISSIONER FOR PATENTS
P.O. Box 1450
Alexandria, Virginia 22313-1450

SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

Dear Sir:

Pursuant to 37 C.F.R. 1.56, Applicants bring to the attention of the Examiner the five (5) references listed in the attached Substitute for Form PTO-1449A/PT. A copy of each is enclosed herein.

These references were cited in related U.S. patent applications. This Information Disclosure Statement is being filed under 37 CFR 1.97(b) prior to the receipt of a first office action.

Citation of the above documents shall not be construed as an admission that the documents are necessarily prior art with respect to the instant invention. Citation of the above documents shall not be construed as a representation that a search has been made other than as described above. Also, the citation of the above documents shall not be construed as an admission that the information cited herein is, or is considered to be, material to patentability as defined in §1.56(b).

Respectfully submitted,

Justin Liu
Attorney for Applicants
Reg. No. 51,959

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Julie Matthews
Name

Signature

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| Substitute for form 1449A/PTO INFORMATION DISCLOSURE STATEMENT BY APPLICANT (use as many sheets as necessary) | | | Complete if Known | | |
| | | | Application / Conf. No. | 10/783,216 / | |
| | | | Filing Date | February 20, 2004 | |
| | | | First Named Inventor | Tim Tuan | |
| | | | Art Unit | Unknown | |
| | | | Examiner Name | Unknown | |
| Sheet | 1 | of | 2 | Attorney Docket Number | X-1462-1P US |

| U.S. PATENT DOCUMENTS | | | | | |
|-----------------------|-----------|-------------------------------|-----------------------------|---|---|
| Examiner Initials * | Cite No.¹ | Document Number | Publication Date MM-DD-YYYY | Name of Patentee or Applicant of Cited Document | Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear |
| | | Number - Kind Code (if known) | | | |
| | | US- 10/783,589 | | Look et al. | Filed 2-20-2004 |
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| FOREIGN PATENT DOCUMENTS | | | | | | |
|--------------------------|-----------|-------------------------|----------------------------------|-----------------------------|---|---|
| Examiner Initials * | Cite No.¹ | Foreign Patent Document | | Publication Date MM-DD-YYYY | Name of Patentee or Applicant of Cited Document | Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear |
| | | Country Code ² | Number ² -Kind Code ³ (if known) | | | |
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| Examiner Signature | Date Considered |
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*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

¹ Applicant's unique citation designation number (optional). ² See Kinds of USPTO Patent Documents at www.uspto.gov or MPEP 901.04. ³ Enter Office that issued the document, by the two-letter code (WIPO Standard ST.3). ⁴ For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. ⁵ Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST. 16 if possible. ⁶ Applicant is to place a check mark here if English language Translation is attached.

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| | | | | Art Unit | Unknown |
| | | | | Examiner Name | Unknown |
| Sheet | 2 | of | 2 | Attorney Docket Number | X-1462-1P US |

| OTHER -- NON PATENT LITERATURE DOCUMENTS | | | |
|--|--------------------------|--|----------------|
| Examiner Initials * | Cite No. ¹ | Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published. | T ² |
| | | MUTOH, S. et al., "1-V Power Supply High-Speed Digital Circuit Technology with Multithreshold-Voltage CMOS," August 1995, pages 847-854, Vol. 30, No. 8, available from IEEE Journal of Solid-State Circuits, 3 Park Avenue, 17th Floor, New York, NY 10016-5997. | |
| | | KURODA, T., et al., "A 0.9V, 150-MHz, 1-mW, 4 mm ² 2-D Discrete Cosine Transform Core Processor with Variable Threshold-Voltage (VT) Scheme, 1996, pages 1770-1779, Vol. 31, No. 11, available from IEEE Journal of Solid-State Circuits, IEEE, 3 Park Avenue, 17th Floor, New York, NY 10016-5997. | |
| | | INUKAI, T. et al., "Boosted Gate MOS (BG MOS): Device/Circuit Cooperation Scheme to Achieve Leakage-Free Giga-Scale Integration," 2000, pages 409-412, available from IEEE Journal of Solid-State Circuits, IEEE, 3 Park Avenue, 17th Floor, New York, NY 10016-5997. | |
| | | HAMZAOGLU, F. et al., "Circuit-Level Techniques to Control Gate Leakage for sub-100nm CMOS," ISLPED, August 12-14, 2002, Pages 60-63, available from IEEE Journal of Solid-State Circuits, IEEE, 3 Park Avenue, 17th Floor, New York, NY 10016-5997. | |
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| Examiner Signature | | Date Considered | |
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¹ Applicant's unique citation designation number. ² Applicant is to place a check mark here if English language Translation is attached.

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